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1230

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

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U.S. UTILITY Patent Application

KS O.I.P.E. SB/3 21 1/15 Q.A. FM	PATENT DATE
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APPLICATION NO. 09/751659	CONT/PRIOR	CLASS 718 715 77	SUBCLASS 100 012	ART UNIT 212 015 517	EXAMINER <u>VERLINDA</u> 01 TANG
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TITLE **APPLICANTS**

Dr. Ivan Mikstich

Parallel dispatch wait signaling method, method for reducing contention of highly contended dispatcher lock, and related operating systems, multiprocessor computer systems and products

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED _____ _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE <div>Amount Due Date Paid</div>	
<input type="checkbox"/> The terminal ____months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER _____	

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